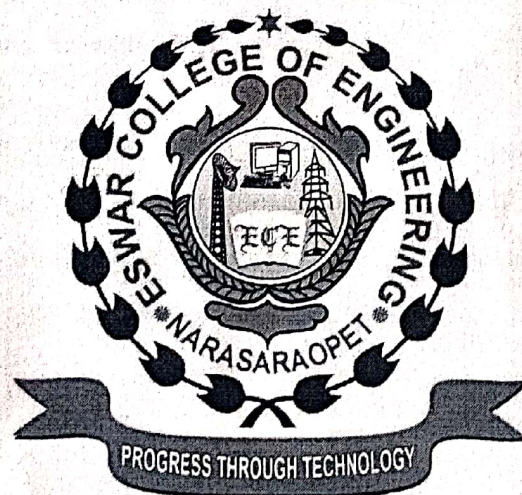


**Resolutions of the
1st BOS Meeting of the EEE
2025-26**



Date: 24.02.2026

Time: 2:30 PM

Mode: Hybrid Mode



ESWAR COLLEGE OF ENGINEERING

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Estd:2008

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

1. AGENDA - FIRST BOARD OF STUDIES MEETING

Agenda items 2026/ EEE /BOS-1

- 1.1 Welcome of the Members and Invitees of the board of studies meeting
- 1.2 Address by the Chairperson of the Board of Studies- EEE
- 1.3 To ratify the course Structure UG(Semester-wise) for EEE
- 1.4 To ratify the course Structure PG(Semester-wise) for EEE(Power Electronics & Electrical Drives)
- 1.5 Ratification of Academic Regulations, and Credit Structure
- 1.6 Ratification of Assessment and Evaluation Pattern(UG) &Approval of panel of examination for question paper setter for end semester examination of m tech(PG).
- 1.7 Any Other Item with the Permission of the Chair
- 1.8 Vote of thanks

Resolutions of the 1st BOS Meeting of the EEE of
Eswar College of Engineering (Autonomous), Kesanupalli held on 24.02.2026
(Through Hybrid Mode)

Members Present

S.No	Name of the Member	Designation/Occupation	Category
1	Dr. V. Praveen	Head of the Department	Chairperson
2	K. Narandra	Assistant professor	One Faculty from each specialization from the College
3	K. Naresh kumar	Assistant professor	
4	D. Venkateswarlu	Assistant professor	
5	M. Epsi	Assistant professor	
6	R. Aruna kumari	Assistant professor	
7	Dr. D. M. Vinod Kumar	Senior Professor in Electrical Engineering NITWARANGAL	Subject experts from out of parent university
8	Dr .A. Raghu Ram	Professor & Vice-Principal Electrical & Electronics Engineering JNTUH University College of Engineering, Science & Technology Hyderabad	
9	Dr. Y. Srinivasa Kishore Babu	Professor of EEE& Vice Principal, UCEN, JNTUK NARASARAOPET	University Nominee
10	N.V.V.S. N Murthy	Sarada Metals & Alloys Limited Deputy manager Dept. of Engineering/E&I	Industry
11	N.V.SIVA KUMAR	SOFTWARE IN TCS Edge processing Management HMI Device & Automotive Testing Engineer Bangalore	Alumni

Members Absent: Nil

The quorum being present, the Chairperson called the meeting to order.

MINUTES OF THE FIRST BOARD OF STUDIES MEETING

Department of Electrical and Electronics Engineering 2026/ EEE /BOS-1

The First Board of Studies meeting of the Department of Electrical and Electronics Engineering was conducted as per the scheduled agenda. The Chairperson welcomed all internal members, external academicians, industry experts, university nominees, and invitees. The following agenda items were discussed and the resolutions were recorded.

Item No. 1.1 – Welcome of the Members and Invitees

The Chairperson extended a warm welcome to all the members of the Board of Studies and highlighted the importance of collaborative academic planning.

Resolution:

The Board noted the welcome note and commencement of the First Board of Studies Meeting.

Item No. 1.2 – Address by the Chairperson of the Board of Studies

The Chairperson addressed the members regarding the academic vision of the EEE programme, industry requirements, and the need for outcome-based education.

Resolution:

The Board noted the Chairman's address and acknowledged the academic directions proposed for strengthening the EEE .

Item No. 1.3 –To ratify the course Structure UG(Semester-wise) for EEE

The semester-wise course structure for I B.Tech (I SEM and II SEM) (R23) was presented, including course distribution, credits, and curriculum design aligned with current industry trends.

Resolution:

After detailed discussion, the Board ratified the draft course structure for implementation from the forthcoming academic batch.

Item No. 1.4–To ratify the course Structure PG(Semester-wise) for EEE(Power Electronics & Electrical Drives)

The semester-wise course structure for I M.Tech (I SEM and II SEM) (R25) was presented, including course distribution, credits, and curriculum design aligned with current industry trends. The detailed syllabus of core theory courses and laboratory courses were presented and discussed. Suggestions were provided by external experts to enhance practical exposure and research orientation.

Resolution: After detailed discussion, the Board ratified the draft course structure for implementation from the forthcoming academic batch, and the finalized syllabus for EEE core courses and laboratories with suggested modifications.

Item No. 1.5 – Ratification of Academic Regulations, and Credit Structure

The academic regulations, credit distribution, L–T–P structure were discussed in detail.

Resolution:

The Board ratified the Academic Regulations, and Credit Structure for the EEE

Item no.1.6 Ratification of Assessment and Evaluation Pattern(UG) & Approval of panel of examination for question paper setter for end semester examination of m tech(PG).

We are presenting the internal and external marks and evaluation for (UG,PG), subject experts names

Resolution

The Board ratified and suggested the list of paper setters.

Item No. 1.7 – Any Other Item with the Permission of the Chair

The Board discussed and several valuable suggestions were proposed by the honourable members under this agenda item.

Dr. Y. Srinivasa Kishore Babu , Professor of EEE & Vice- Principal ,Ucen ,Jntuk Narasaraopet , University Nominee observed that the department is presently following the JNTUK syllabus and advised to continue the same until further academic revisions are implemented.

Dr .A. Raghu Ram , Professor & Vice-Principal , JNTUH University College of Engineering, Science & Technology Hyderabad , observed that the department is presently following the JNTUK syllabus and advised to continue the same until further academic revisions are implemented.

Dr. D. M. Vinod Kumar , Senior Professor in Electrical Engineering NIT Warangal recommended initiating student chapters and organizing workshops and seminars to strengthen innovation, technical exposure, and collaborative learning among students.

N.V.V.S.N.Murthy ,Sarada Metals &Alloys Limited Deputy manager Dept.of Engineering/E&I suggested introducing Professional Grooming & core subjects, establishing a industries programs and implementing a Structured English Communication Lab to enhance students' industry readiness and professional skills.

After detailed deliberations, the Board ratified the suggestions of the members and resolved to consider the implementation of the proposed activities in a phased manner, while continuing with the existing JNTUK syllabus as recommended.

Vote of thanks

The meeting has concluded with a vote of thanks proposed by Dr. V.Praveen, expressing sincere gratitude to all the members for their valuable guidance and participation.
The meeting ended at 3:30 PM with the consent of the Chair.



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Estd:2008

DEPARTMENT OF

ELECTRICAL AND ELECTRONIC SENGINEERING OF BOS

S.No	Name of the Member	Designation/Occupation	Category	Attendance
1	Dr. V. Praveen	Head of the Department	Chairman	<i>V. Praveen</i>
2	K. Nerendra	Assistant professor	One Faculty from each specialization from the College	<i>Nerendra</i>
3	K. Naresh kumar	Assistant professor		<i>K. Nareshkumar</i>
4	D. Venkateswarlu	Assistant professor		<i>D. Venkateswarlu</i>
5	M. Epsi	Assistant professor		<i>M. Epsi</i>
6	R. Aruna kumari	Assistant professor		<i>Aruna</i>
7	Dr. D. M. Vinod Kumar	Senior Professor in Electrical Engineering NITWARANGAL	Subject experts from out of parent university	Approved through Email
8	Dr .A. Raghu Ram	Professor & Vice-Principal Electrical & Electronics Engineering JNTUH University College of Engineering, Science & Technology Hyderabad		Approved through Email
9	Dr. Y. Srinivasa Kishore Babu	Professor of EEE& Vice Principal UCEN,JNTUK NARASARAOPET	University Nominee	Approved through Email
10	N.V.V.S. N Murthy	Sarada Metals & Alloys Limited Deputy manager Dept. of Engineering/E&I	Industry	Approved through Email
11	N.V.SIVA KUMAR	System Engineer at TCS Edge processing Management HMI Device & Automotive Testing Engineer Bangalore	Alumni	Approved through Email



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Accredited by NAAC with "B++" Grade

Kesanupalli (V), Narasaraopet (M), Palnadu (D), A.P - 522 601

Department of Electrical and Electronics Engineering

Date: 24-02-2026

To

Dr. Y. Srinivasa Kishore Babu,
Associate Professor, Dept. of EEE,
UCEN, JNTUK Narasaraopet,
University BOS Nominee,
Department of Electrical and Electronics Engineering,
Eswar College of Engineering (Autonomous),
Narasaraopet,
Palnadu District.

Respected Sir/Madam,

Subject: Invitation to Board of Studies Meeting in Hybrid Mode on 24-02-2026 at 2:00PM - Reg

Greetings from Department of EEE, Eswar College of Engineering.

We are pleased to inform you that the Board of Studies of Department of EEE, Eswar College of Engineering is scheduled to be held on 24-02-2026. The meeting will be conducted in Hybrid Mode to ensure the convenience and participation of all esteemed members.

Meeting Details:

Date: 24-02-2026

Time: 02:30 PM

Mode: Hybrid (Physical Meeting at the College Campus / Online through provided link)

Venue (Physical): EEE Department BOS, Eswar College of Engineering.

Online Link: Will be shared separately prior to the meeting

We kindly request your gracious presence and active participation in the meeting. Your valuable insights and guidance are essential for the effective functioning and progress of the institution. Kindly confirm your availability at your earliest convenience.

We look forward to your participation.

Thank you

Yours Sincerely
Dr. V. Praveen
HOD-EEE,
ESWAR College of Engineering

Encl:

1. Agenda
2. Details of the BoS Members
3. I M-Tech Course Structure



2/26/26, 2:16 PM

Re: EEE BOS MEETING OF MOM

Re: EEE BOS MEETING OF MOM

From: Vinod Kumar D M <vinodkumar.dm@gmail.com>

To: hodeee@eswarcollegeofengg.org

cc: raghuram-a@jntuch.ac.in, murthy.nagulakonda@gmail.com, nvsivakumar07@gmail.com, yakbabu.eee@jntukucen.ac.in

Date: 2026-02-26 11:26

Approved the Minutes of the meeting of BoS.

Dr. D.M. VINOD KUMAR
Professor (H.A.G.)
Former Dean (Academic) & Head
Department of Electrical Engineering
National Institute of Technology Warangal (NITW)
WARANGAL - 506 004
Telangana State
INDIA

Mobile No: 09885294521

e-mail: dmvk@nitw.ac.in

vinodkumar.dm@gmail.com

<http://www.nitw.ac.in/faculty/id/16226/>

On Thu, 26 Feb 2026, 11:12 , <hodeee@eswarcollegeofengg.org> wrote:

GOOD MORNING SIR, I am from EEE Dept, Eswar College of Engineering
sir, I am sending you the minutes of the meeting . if you have any remarks or modifications ,if there is no modifications
please send an acceptance letter by email, sir

2/26/26, 9:43 AM

Re: EEE BOS FROM MOM

Re: EEE BOS FROM MOM

From: Dr. A. Raghu Ram <raghuram_a@jntuh.ac.in>
To: hodeee@eswarcollegeofengg.org
Date: 2026-02-26 15:37

Dear sir,

I am herewith accepting the above minutes of the B.O.S meeting held on 24-02-2026 A.N.

with regards,
Dr.A.Raghu Ram

On Thu, Feb 26, 2026 at 12:13 PM <hodeee@eswarcollegeofengg.org> wrote:
GOOD MORNING SIR, I am from EEE Dept, Eswar College of Engineering
sir, I am sending you the minutes of the meeting . if you have any remarks or modifications ,if there is no
modifications please send an acceptance latter by email, sir

Re: EEE BOS MEETING FROM MOM

From: Dr. Y. S. Kishore Babu EEE, JNTUK UCEN <yskbabu.eee@jntukucen.ac.in>

To: hodeee@eswarcollegeofengg.org

Date: 2026-02-27 16:00

Approved, please proceed.

With Best Regards

Dr.Y.S.Kishore Babu

SMIEEE

Professor of EEE & Vice Principal

University College of Engineering JNTUK Narasaraopet

KAKANI(V),Palnadu(District)-522603

Andhra Pradesh, INDIA

Ph:+91 9440518404/9673646555

On Fri, 27 Feb 2026 at 15:55, <hodeee@eswarcollegeofengg.org> wrote:

SORRY FOR THE MISTAKE SIR

I REQUEST YOU SIR PLEASE APPROVED THIS MOM

On Fri, Feb 27, 2026 at 3:14 PM Dr. Y. S. Kishore Babu EEE, JNTUK UCEN <yskbabu.eee@jntukucen.ac.in> wrote:

Dear sir,

Greetings.

Name correction against my name.please modify and resend the file. i will approve PFA for necessary corrections

With Best Regards

Dr.Y.S.Kishore Babu

SMIEEE

Professor of EEE & Vice Principal

University College of Engineering JNTUK Narasaraopet

KAKANI(V),Palnadu(District)-522603

Andhra Pradesh, INDIA

Ph:+91 9440518404/9673646555

On Thu, 26 Feb 2026 at 15:18, <hodeee@eswarcollegeofengg.org> wrote:

GOOD MORNING SIR, I am from EEE Dept, Eswar College of Engineering

sir, I am sending you the minutes of the meeting . if you have any remarks or modifications ,if there is no modifications please send an acceptance latter by email, sir

2/27/26, 3:32 PM

Regarding Minutes of meeting of BoS

Regarding Minutes of meeting of BoS

From: murthy nagulakonda <murthy.nagulakonda@gmail.com>

To: hodeee@eswarcollegeofengg.org

Date: 2026-02-27 15:01

Approved the Minutes of the meeting of BoS

N.V.S.N.Murthy,
Deputy Manager,
E&I department,
Sarda Metals and Alloys Ltd,
Mobile No:8897439015.



2/26/26, 3:08 PM

Re: EEE BOS MEETING OF MOM

Re: EEE BOS MEETING OF MOM

From: nuthalapati venkatasivakumar <nvsivakumar07@gmail.com>

To: hodeeee@eswarcollegeofengg.org

cc: vinodkumar.dm@gmail.com, raghuram-a@jntuch.ac.in, murthy.nagulakonda@gmail.com, yakbabu.eee@jntukucen.ac.in

Date: 2026-02-26 14:43

Approved the Minutes of the meeting of BoS.

Thanks & Regards

Nuthalapati Venkata Sivakumar

nvsivakumar07@gmail.com

TCS Bangalore

On Thu, Feb 26, 2026, 11:12 AM <hodeeee@eswarcollegeofengg.org> wrote:

GOOD MORNING SIR, I am from EEE Dept, Eswar College of Engineering
sir, I am sending you the minutes of the meeting . if you have any remarks or modifications ,if there is no modifications
please send an acceptance latter by email, sir



R-25 Syllabus for M.Tech (PEED)- ESWAR w. e. f. 2025 -26

ESWAR COLLEGE OF ENGINEERING (AUTONOMOUS)

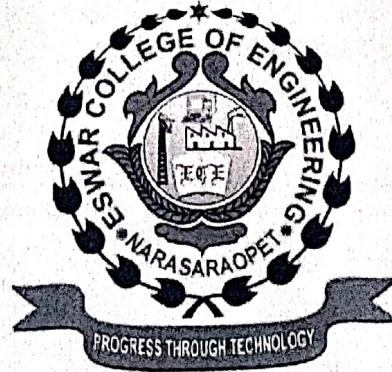
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
R25 M.TECH PEED COURSE STRUCTURE AND SYLLABUS

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

COURSE STRUCTURE & SYLLABUS FOR

R25 M.Tech (PEED) PROGRAM

(Applicable for batches admitted from 2025-2026)



ESWAR COLLEGE OF ENGINEERING, NARASARAO PET



R-25 Syllabus for M.Tech (PEED)- ESWAR w. e. f. 2025 -26

ESWAR COLLEGE OF ENGINEERING (AUTONOMOUS)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
R25 M.TECH PEED COURSE STRUCTURE AND SYLLABUS

M. Tech (PEED)

I-SEMESTER

S. No.	Course code	Course Title	L	T	P	C
1	D251AT01	Program Core – 1 Electrical Machine Modeling and Analysis	3	1	0	4
2	D251AT02	Program Core – 2 Power Electronic Converters	3	1	0	4
3	D251AT03	Program Core – 3 Electric Vehicles	3	1	0	4
4	D251ATA1 D251ATA2 D251ATA3	Program Elective – I i. Modern Control Theory ii. Power Quality Enhancement using Custom Power Devices iii. Industrial Control Electronics	3	0	0	3
5	D251ATB1 D251ATB2 D251ATB3	Program Elective – II i. Artificial Intelligence Techniques ii. Renewable Energy Technologies iii. HVDC Transmission and Flexible AC Transmission Systems	3	0	0	3
6	D251AT04	Laboratory – 1 Power Converters Simulation Laboratory	0	1	2	2
7	D251AT05	Laboratory – 2 Power Converters Laboratory	0	1	2	2
8	D251AT06	Seminar-I	0	0	2	1
TOTAL			15	5	6	23

**ESWAR COLLEGE OF ENGINEERING
(AUTONOMOUS)**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
R25 M.TECH PEED COURSE STRUCTURE AND SYLLABUS**II-SEMESTER**

S. No.	COURSE CODE	Course Title	L	T	P	C
1	D252AT01	Program Core – 4 Switched Mode Power Conversion	3	1	0	4
2	D252AT02	Program Core – 5 Power Electronic Control of Electrical Drives	3	1	0	4
3	D252AT03	Program Core – 6 Digital Controllers for Power Electronic Applications	3	1	0	4
4	D252ATA1 D252ATA2 D252ATA3	Program Elective – III i. Control & Integration of Renewable Energy Systems ii. Digital Control Systems iii. Battery Management Systems and Charging Stations	3	0	0	3
5	D252ATB1 D252ATB2 D252ATB3	Program Elective – IV i. Advanced Digital Signal Processing ii. Applications of Power Converters iii. Industrial Internet of Things	3	0	0	3
6	D252AT04	Laboratory – 3 Electric Drives Simulation Laboratory	0	1	2	2
7	D252AT05	Laboratory – 4 Digital Controllers and Electric Drives Laboratory	0	1	2	2
8	D252AT06	Seminar – II	0	0	2	1
TOTAL			15	5	6	23

Credit Structure

Course Type	Course Title	L	T	P	Credits
Theory Course	Basic Electrical and Electronics Engineering	3	0	0	3
Theory Course	Network Analysis	3	0	0	3
Theory Course	Electrical Circuit Analysis-II	3	0	0	3
Laboratory Course	Electrical and Electronics Engineering workshop	0	0	3	1.5
Laboratory Course	Network Analysis Lab	0	0	3	1.5
Laboratory Course	Electrical Circuit Analysis-II Lab	0	0	3	1.5